

Seminar

Trends in Computer-Aided Verification

Introduction

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Software Modeling and Verification Group



`noll@cs.rwth-aachen.de`

Winter Semester 2014/15; 15 October, 2014

- 1 Overview
- 2 Aims of this Seminar
- 3 Important Dates
- 4 Seminar Topics
- 5 Christina Jansen: Axiomatic Verification
- 6 Thomas Noll: Graph-Based Abstraction
- 7 Tim Lange: Inductive Incremental Verification
- 8 Kevin van der Pol: Verification of Probabilistic Systems
- 9 Final Hints

Formal verification methods

- **Rigorous, mathematically based techniques** for the specification, development and verification of software and hardware systems
- Aim at improving **correctness, reliability and robustness** of such systems

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Classifications

- According to **design phase**
 - specification, implementation, testing, ...
- According to **specification formalism**
 - process algebras, timed automata, Markov chains, ...
- According to underlying **mathematical theories**
 - model checking, theorem proving, static analysis, ...

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Aims of this seminar

- **Independent understanding** of a scientific topic
- Acquiring, reading and understanding **scientific literature**
- Writing of your **own report** on this topic
- **Oral presentation** of your results

Requirements on Report

Your report

- Independent writing of a report of **15–20 pages**
- **Complete** set of references to all consulted literature
- **Correct citation** of important literature
- **Plagiarism**: taking text blocks (from literature or web) without source indication causes immediate **exclusion from this seminar**
- Font size **12pt** with “normal” page layout
- **Language**: German or English
- We expect the **correct usage** of spelling and grammar
 - ≥ 10 errors per page \implies abortion of correction

Your talk

- Talk of about **45 minutes**
- Focus your talk on the **audience**
- **Descriptive** slides:
 - ≤ 15 lines of text
 - use (base) colors in a useful manner
- **Language:** German or English
- No spelling mistakes please!
- Finish **in time**. Overtime is bad
- Ask for **questions**

Preparation of your talk

- Setup laptop and projector **ahead** of time
- Use a (laser) **pointer**
- **Number** your slides
- Multiple **copies**: laptop, USB, web
- Have **backup slides** ready for expected questions

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Talks

The seminar will be held weekly on **Tuesdays at 16:00 (?)** starting end of November

- see <http://moves.rwth-aachen.de/teaching/ws-1415/cav/>

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Deadlines

You are requested to adhere to the following **firm deadlines**:

- immediately: obtain the required **literature** from the web or library
- **eight** weeks before your talk: present a table of contents
- **six** weeks before your talk: preliminary version of your report
- **four** weeks before your talk: final version of your report
- **two** weeks before your talk: preliminary version of your slides
- **one** week before your talk: final version of your slides

Important Dates

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Missing a deadline causes **immediate exclusion** from the seminar

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Selecting Your Topic

Procedure

- You obtain(ed) a list of topics of this seminar.
- Indicate the preference of your topics (first, second, third).
- We do our best to find an adequate topic-student distribution.
- Disclaimer: no guarantee for an optimal solution.
- Your topic will be published on our website by **17 October**.
- Please give language preference
 - unsure \implies German

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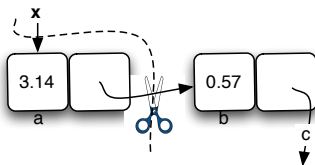
Withdrawal

- You have up to **three weeks** to refrain from participating in this seminar.
- Later cancellation (by you or by us) causes a **not passed** for this seminar and reduces your (three) possibilities by one.

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1: Permission Accounting in Separation Logic

(Richard Bornat, Cristiano Calcagno, Peter O'Hearn, and Matthew Parkinson)



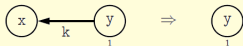
- verification of concurrent heap-manipulating programs
- heap described by (Separation) logic formulae
- challenge addressed: shared memory
- permissions guarantee data-race freedom

2: Hoare-Style Verification of Graph Programs

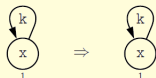
(Christopher M. Poskitt and Detlef Plump)

```
main = RemoveLeaf!; if NotNull then No else Yes.  
NotNull = {Loop, EdgeBetweenNodes}.
```

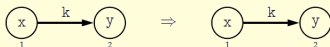
RemoveLeaf($x, y, k : \text{int}$) =



Loop($x, k : \text{int}$) =



EdgeBetweenNodes($x, y, k : \text{int}$) =



- visual programming: GP
- programming language based on graph transformation
- specification: structural graph properties and label properties

3: Implicit Dynamic Frames

(Jan Smans, Bart Jacobs, and Frank Piessens)



We wish to logically represent how the execution of a command changes the state without having to explicitly say how the command does not change the state.

- verification of OO programs
- pre-/postcondition based
- program state as FO-formula
- enrich pre-/postconditions with access assertions (“the heap part a method is allowed to touch”)

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4: Grammar-Based Shape Analysis

(Oukseh Lee, Hongseok Yang, and Kwangkeun Yi)

Verification of pointer programs

- Infinite data domains
 - Dynamic storage (de-)allocation
- ⇒ Requires **abstraction** techniques

Heap:

$x \rightarrow o_1 \xrightarrow{n} o_2 \xrightarrow{n} o_3 \xrightarrow{n} \text{nil}$

Shape graph:

$\{x\} \xrightarrow{n} \emptyset \circlearrowleft n$

Shape analysis

- Based on “instrumentation predicates”
 - Yields partition of concrete nodes (“summary nodes”)
- ⇒ Information loss in summary nodes

Refinement: grammar annotations

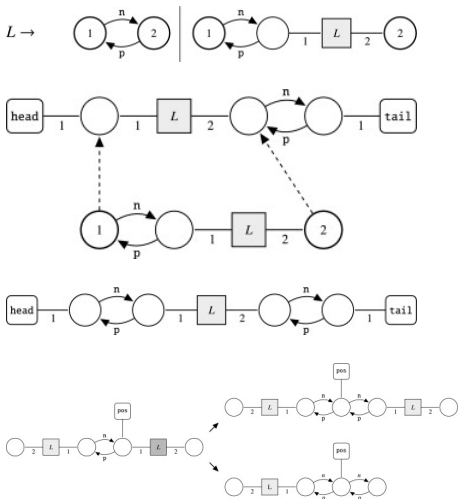
- Idea: associate tree grammars with summary nodes
- Applications: binomial heaps, Schorr-Waite tree traversal

5: Abstraction by Hyperedge Replacement Grammars

(Jonathan Heinen, Christina Jansen, Joost-Pieter Katoen, and Thomas Noll)

Approach

- **Hyperedge replacement grammars** for modelling dynamic data structures
- **Abstraction** = backward application of rule
- **Concretisation** = forward application of rule
- Yields finite state space
- Makes standard **model checking** applicable
- Application: stackless tree traversal



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6: Software Model Checking via IC3

(Alessandro Cimatti and Alberto Griggio)

IC3

- Hardware MC approach
- construct inductive reachability sets
- look for violations of induction (CTI)
- trace back from CTIs
 - unreachable: exclude
 - reachable: trace back further

Software Model Checking

- Adapt Hardware MC approach to Software MC
- Combine with well understood techniques from Software MC
 - ART construction
 - Interpolation

7: IC3 Modulo Theories via Implicit Predicate Abstraction

(Alessandro Cimatti, Alberto Griggio, Sergio Mover, and Stefano Tonnetta)

IC3

- Hardware MC approach
- Finite set of variables
- Binary circuits
- Finite state space
- Excluding states always terminates

Software Model Checking

- Control + data flow
- Finite set of variables
- Infinite variable domain
- Infinite state space
- Excluding states will not terminate

- Use Predicate Abstraction to project infinite state space to finite one
- Use original Boolean IC3 algorithm

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(Anvesh Komuravelli, Corina S. Păsăreanu, and Edmund M. Clarke)

- Model with multiple components: state space explosion

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Assume-Guarantee rule

$$\frac{C_1 \models A \qquad C_2 \models A \Rightarrow P}{C_1 \parallel C_2 \models P}$$

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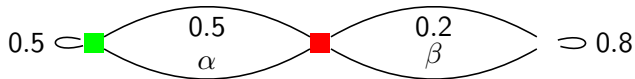
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- Problem: find A

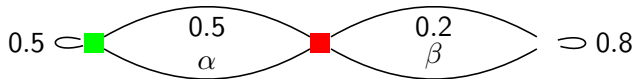
9: Analysis of Markov Decision Processes

(Krishnendu Chatterjee)



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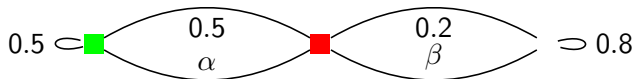
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- Multiple-objective Long Run Average

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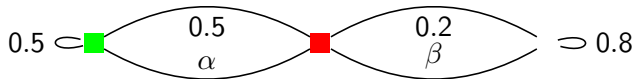
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- Multiple-objective Long Run Average
- $\mathcal{M} \models \text{green} \geq 0.5$

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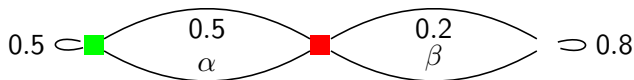
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- $\mathcal{M} \models \text{red} \leq 0.2$

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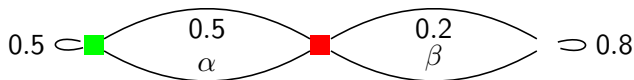
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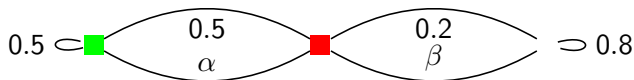
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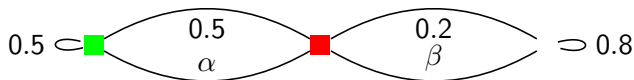
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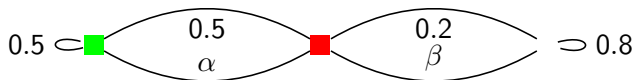
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$$\frac{C_1 \models A \quad C_2 \not\models A \wedge \neg P}{C_1 \parallel C_2 \models P}$$

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We wish you success and look forward to an enjoyable and high-quality seminar!