



Semantics and Verification of Software

Summer Semester 2015

Lecture 12: Axiomatic Semantics of WHILE IV (Axiomatic Equivalence)

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<http://moves.rwth-aachen.de/teaching/ss-15/sv-sw/>

Recap: Partial & Total Correctness Properties

Outline of Lecture 12

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Axiomatic Equivalence

Characteristic Assertions

Partial vs. Total Equivalence

Axiomatic vs. Operational/Denotational Equivalence

Summary: Axiomatic Semantics

Recap: Partial & Total Correctness Properties

Hoare Logic

Goal: syntactic derivation of valid partial correctness properties. Here $A[x \mapsto a]$ denotes the syntactic replacement of every occurrence of x by a in A .



Tony Hoare (* 1934)

Definition (Hoare Logic)

The **Hoare rules** are given by

$$\begin{array}{c} \text{(skip)} \frac{}{\{A\} \text{ skip } \{A\}} \\ \text{(seq)} \frac{\{A\} c_1 \{C\} \quad \{C\} c_2 \{B\}}{\{A\} c_1 ; c_2 \{B\}} \\ \text{(while)} \frac{\{A \wedge b\} c \{A\}}{\{A\} \text{ while } b \text{ do } c \text{ end } \{A \wedge \neg b\}} \\ \text{(asgn)} \frac{}{\{A[x \mapsto a]\} x := a \{A\}} \\ \text{(if)} \frac{\{A \wedge b\} c_1 \{B\} \quad \{A \wedge \neg b\} c_2 \{B\}}{\{A\} \text{ if } b \text{ then } c_1 \text{ else } c_2 \text{ end } \{B\}} \\ \text{(cons)} \frac{\models (A \Rightarrow A') \quad \{A'\} c \{B'\} \quad \models (B' \Rightarrow B)}{\{A\} c \{B\}} \end{array}$$

A partial correctness property is **provable** (notation: $\vdash \{A\} c \{B\}$) if it is derivable by the Hoare rules. In (while), A is called a **(loop) invariant**.

Recap: Partial & Total Correctness Properties

Proving Total Correctness

Goal: syntactic derivation of valid total correctness properties

Definition (Hoare Logic for total correctness)

The **Hoare rules for total correctness** are given by (where $i \in LVar$)

$$\begin{array}{c} \text{(skip)} \frac{}{\{A\} \text{ skip } \{\Downarrow A\}} \\ \text{(seq)} \frac{\{A\} c_1 \{\Downarrow C\} \quad \{C\} c_2 \{\Downarrow B\}}{\{A\} c_1 ; c_2 \{\Downarrow B\}} \\ \text{(while)} \frac{\vdash (i \geq 0 \wedge A(i+1) \Rightarrow b) \quad \{i \geq 0 \wedge A(i+1)\} c \{\Downarrow A(i)\} \quad \vdash (A(0) \Rightarrow \neg b)}{\{\exists i. i \geq 0 \wedge A(i)\} \text{ while } b \text{ do } c \text{ end } \{\Downarrow A(0)\}} \\ \text{(cons)} \frac{\vdash (A \Rightarrow A') \quad \{A'\} c \{\Downarrow B'\} \quad \vdash (B' \Rightarrow B)}{\{A\} c \{\Downarrow B\}} \\ \text{(asgn)} \frac{}{\{A[x \mapsto a]\} x := a \{\Downarrow A\}} \\ \text{(if)} \frac{\{A \wedge b\} c_1 \{\Downarrow B\} \quad \{A \wedge \neg b\} c_2 \{\Downarrow B\}}{\{A\} \text{ if } b \text{ then } c_1 \text{ else } c_2 \text{ end } \{\Downarrow B\}} \end{array}$$

A total correctness property is **provable** (notation: $\vdash \{A\} c \{\Downarrow B\}$) if it is derivable by the Hoare rules. In case of (while), $A(i)$ is called a **(loop) invariant**.

Axiomatic Equivalence

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Operational and Denotational Equivalence

Definition 4.1: $\mathcal{D}[\cdot]$: $Cmd \rightarrow (\Sigma \dashrightarrow \Sigma)$ given by

$$\mathcal{D}[c]\sigma = \sigma' \iff \langle c, \sigma \rangle \rightarrow \sigma'$$

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$$\mathcal{D}[c_1] = \mathcal{D}[c_2]$$

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Theorem 8.5: For every $c \in Cmd$,

$$\mathcal{D}[c] = \mathcal{C}[c]$$

Axiomatic Equivalence

Axiomatic Equivalence I

In the axiomatic semantics, two statements have to be considered equivalent if they are **indistinguishable** w.r.t. partial correctness properties:

Definition 12.1 (Axiomatic equivalence)

Two statements $c_1, c_2 \in \text{Cmd}$ are called **axiomatically equivalent** (notation: $c_1 \approx c_2$) if, for all assertions $A, B \in \text{Assn}$,

$$\models \{A\} c_1 \{B\} \iff \models \{A\} c_2 \{B\}.$$

Axiomatic Equivalence II

Example 12.2

We show that `while b do c end` \approx `if b then c; while b do c end else skip end` (cf. Lemma 4.3).

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Characteristic Assertions

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Characteristic Assertions

Characteristic Assertions I

The following results are based of the following **encoding of states** by assertions:

Definition 12.3

Given a finite subset of program variables $X \subseteq \text{Var}$ and a state $\sigma \in \Sigma$, the **characteristic assertion of σ w.r.t. X** is given by

$$\text{State}(\sigma, X) := \bigwedge_{x \in X} (x = \underbrace{\sigma(x)}_{\in \mathbb{Z}}) \in \text{Assn}$$

Moreover, we let $\text{State}(\sigma, \emptyset) := \text{true}$ and $\text{State}(\perp, X) := \text{false}$.

Characteristic Assertions

Characteristic Assertions II

Programs and characteristic state assertions are obviously related in the following way:

Corollary 12.4

Let $c \in \text{Cmd}$, and let $FV(c) \subseteq \text{Var}$ denote the set of all variables occurring in c . Then, for every finite $X \supseteq FV(c)$ and $\sigma \in \Sigma$,

$$\{ \text{State}(\sigma, X) \} c \{ \text{State}(\mathcal{C}[[c]]\sigma, X) \}$$

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$$\{ \text{State}(\sigma, X) \} c \{ \text{State}(\mathcal{C}[[c]]\sigma, X) \}$$

Example 12.5 (Factorial program)

For $c := (y:=1; \text{while } \neg(x=1) \text{ do } y:=y*x; x:=x-1 \text{ end})$, $X = \{x, y\}$, $\sigma(x) = 3$, and $\sigma(y) = 0$, we obtain

$$\begin{aligned} \text{State}(\sigma, X) &= (x=3 \wedge y=0) \\ \text{State}(\mathcal{C}[[c]]\sigma, X) &= (x=1 \wedge y=6) \end{aligned}$$

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Now we can show that considering **total** rather than partial correctness properties yields the same notion of equivalence:

Theorem 12.6

Let $c_1, c_2 \in \text{Cmd}$. The following propositions are equivalent:

$$1. \forall A, B \in \text{Assn} : \models \{A\} c_1 \{B\} \iff \models \{A\} c_2 \{B\}$$

$$2. \forall A, B \in \text{Assn} : \models \{A\} c_1 \{\Downarrow B\} \iff \models \{A\} c_2 \{\Downarrow B\}$$

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Proof.

on the board □

Axiomatic vs. Operational/Denotational Equivalence

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Theorem 12.7

Axiomatic and operational/denotational equivalence coincide, i.e., for all $c_1, c_2 \in \text{Cmd}$,

$$c_1 \approx c_2 \iff c_1 \sim c_2.$$

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- Technically involved (especially loop invariants)
 - ⇒ machine support (**proof assistants**) indispensable for larger programs

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- **Equivalence** of axiomatic and operational/denotational semantics
- **Software engineering** aspect: integrated development of program and proof (cf. assertions in Java)
- Systematic approach: **mechanised program verification**
 1. Start with (correctness) requirements for program
 2. Manually derive corresponding program annotations (assertions)
 3. Automatically derive corresponding verification conditions (using weakest preconditions etc.)
 4. Automatically discharge/simplify verification conditions using theorem prover
 5. Manually complete proof if required

(cf. Mike Gordon: *Background reading on Hoare Logic*, Chapter 3,
www.cl.cam.ac.uk/~mjc/Teaching/2011/Hoare/Notes/Notes.pdf)