

# **Concurrency Theory**

Winter Semester 2015/16

**Lecture 2: Calculus of Communicating Systems (CCS)** 

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http://moves.rwth-aachen.de/teaching/ws-1516/ct/





# The Approach

#### **Outline of Lecture 2**

The Approach

Syntax of CCS

Intuitive Meaning and Examples

Formal Semantics of CCS





### The Approach

### The Calculus of Communicating Systems

#### **History:**

- Robin Milner: A Calculus of Communicating Systems LNCS 92, Springer, 1980
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**Approach:** describing parallelism on a simple and abstract level, using only a few basic primitives

- no explicit storage (variables)
- no explicit representation of values (numbers, Booleans, ...)
- parallel system reduced to communication potential





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- Let Pid be a set of process identifiers.
- The set *Prc* of process expressions is defined by the following syntax:

$$P ::= nil$$
 (inaction)  
 $\mid \alpha.P$  (prefixing)  
 $\mid P_1 + P_2$  (choice)  
 $\mid P_1 \mid\mid P_2$  (parallel composition)  
 $\mid P \setminus L$  (restriction)  
 $\mid P[f]$  (relabelling)  
 $\mid C$  (process call)

where  $\alpha \in Act$ ,  $L \subseteq A$ ,  $C \in Pid$ , and  $f : Act \to Act$  such that  $f(\tau) = \tau$  and  $f(\overline{a}) = f(a)$  for each  $a \in A$ .





Concurrency Theory

### Syntax of CCS II

# Definition 2.1 (continued)

A (recursive) process definition is an equation system of the form

$$(C_i = P_i \mid 1 \leq i \leq k)$$

where  $k \ge 1$ ,  $C_i \in Pid$  (pairwise distinct), and  $P_i \in Prc$  (with identifiers from  $\{C_1, \ldots, C_k\}$ ).



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#### **Notational Conventions:**

- $\bullet \overline{\overline{a}}$  means a
- $\sum_{i=1}^n P_i$   $(n \in \mathbb{N})$  means  $P_1 + \ldots + P_n$  (where  $\sum_{i=1}^0 P_i := \text{nil}$ )
- P \ a abbreviates P \ {a}

Concurrency Theory

- $[a_1 \mapsto b_1, \dots, a_n \mapsto b_n]$  stands for  $f : Act \to Act$  with  $f(a_i) = b_i$  ( $i \in [n]$ ) and  $f(\alpha) = \alpha$  otherwise
- restriction and relabelling bind stronger than prefixing, prefixing stronger than composition, composition stronger than choice:

$$P \setminus a + b \cdot Q \parallel R$$
 means  $(P \setminus a) + ((b \cdot Q) \parallel R)$ 





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# **Meaning of CCS Constructs**

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- The relabelling P[f] allows to adapt the naming of actions.
- The behaviour of a process call C is given by the right-hand side of the corresponding equation.





### **CCS Examples**

# Example 2.2

- 1. One-place buffer
- 2. Two-place buffer
- 3. Parallel specification of two-place buffer

(on the board)





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Goal: represent behaviour of system by (infinite) graph

- nodes = system states
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### Definition 2.3 (Labelled transition system)

A (Act-)labelled transition system (LTS) is a triple  $(S, Act, \longrightarrow)$  consisting of

- a set S of states
- a set Act of (action) labels

Concurrency Theory

• a transition relation  $\longrightarrow \subseteq S \times Act \times S$ 

For  $(s, \alpha, s') \in \longrightarrow$  we write  $s \stackrel{\alpha}{\longrightarrow} s'$ . An LTS is called finite if S is so.





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#### **Remarks:**

- sometimes an initial state  $s_0 \in S$  is distinguished ("LTS( $s_0$ )")
- (finite) LTSs correspond to (finite) automata without final states





#### Semantics of CCS I

We define the assignment

by induction over the syntactic structure of process expressions. Here we employ derivation rules of the form

rule name conclusion

which can be composed to complete derivation trees.





#### Semantics of CCS II

### Definition 2.4 (Semantics of CCS)

A process definition  $(C_i = P_i \mid 1 \le i \le k)$  determines the LTS  $(Prc, Act, \longrightarrow)$  whose transitions can be inferred from the following rules  $(P, P', Q, Q' \in Prc, \alpha \in Act, \lambda \in A \cup \overline{A}, a \in A)$ :

$$(Act) \overline{Q \cdot P \xrightarrow{\alpha} P} \qquad (Sum_1) \overline{P \xrightarrow{\alpha} P'} \qquad (Sum_2) \overline{Q \xrightarrow{\alpha} Q'}$$

$$(Sum_2) \overline{P + Q \xrightarrow{\alpha} Q'}$$

$$(Par_1) \overline{P \parallel Q \xrightarrow{\alpha} P' \parallel Q} \qquad (Par_2) \overline{P \parallel Q \xrightarrow{\alpha} P \parallel Q'} \qquad (Com) \overline{P \xrightarrow{\lambda} P' Q \xrightarrow{\overline{\lambda}} Q'}$$

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#### **Semantics of CCS III**

### Example 2.5

1. One-place buffer:

$$B = in.\overline{out}.B$$

2. Sequential two-place buffer:

$$B_0 = \underline{in.B_1}$$
  
 $B_1 = \overline{out.B_0} + \underline{in.B_2}$   
 $B_2 = \overline{out.B_1}$ 

3. Parallel two-place buffer:

$$B_{\parallel} = (B[f] \parallel B[g]) \setminus com$$
  
 $B = in.out.B$ 

where  $f := [out \mapsto com]$  and  $g := [in \mapsto com]$ 

(on the board)





#### **Semantics of CCS IV**

#### Example 2.5 (continued)

Complete LTS of parallel two-place buffer:



